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3-229 U.S. P.1
10/073196
02/13/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10073196	FILING DATE 02/13/2002	CLASS 364	SUBCLASS 774	GAU 327	EXAMINER 11-1001
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**APPLICANTS: Tsunernasu Kimio;

**CONTINUING DATA VERIFIED:

None

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-034793 02/13/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☐ yes ☒ no

Verified and Acknowledged Examiners's Initials

ATTORNEY DOCKET NO

8037-1001

TITLE : Semiconductor device capable of preventing solder balls from being removed in reinforcing pad
U.S. DEPT. OF COMM. PAT. & TM. PTO-436L (Rev. 12-94)


NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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FILED WITH:

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(Attached in pocket on right inside flap)

Issue Classification 	Application No.	Applicant(s)	
	10/073,196	TSUNEMASU, KIMIO	
	Examiner	Art Unit	
	James M. Mitchell	2827	

ISSUE CLASSIFICATION										
ORIGINAL				CROSS REFERENCE(S)						
CLASS	SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
257	780			257	772	777	778	779		
INTERNATIONAL CLASSIFICATION										
H	0	1	L	29	1	40				
				/						
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				/						
<i>E. Mitchell</i> (Assistant Examiner)				KAMAND CUNEO SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 <i>[Signature]</i> (Primary Examiner)				Total Claims Allowed:		
								O.G. Print Claim(s)	O.G. Print Fig.	
								8	SA, SB	

<input checked="" type="checkbox"/> Claims renumbered in the same order as presented by applicant										<input type="checkbox"/> CPA		<input type="checkbox"/> T.D.		<input type="checkbox"/> R.1.47		
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